



PTO/SB/08B (08/03) (modified)

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Substitute for form 1449A/PTO		Application Number	10/662,872
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	September 16, 2003
(use as many sheets as necessary)		First Named Inventor	Vladimir STJANOVIC, et al.
		Art Unit	2816
		Examiner Name	Unassigned
Sheet	1 of 1	Attorney Docket Number	57941.000025

U.S. PATENT DOCUMENTS

EXAMINER SIGNATURE /Curtis Odom/ **DATE CONSIDERED** 07/05/2006

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OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS					
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				YES	NO
CO	3.	J. M. Hoke, et al., "Self-timed Interface for S/390 I/O Subsystem Interconnection", IBM J. Res. Develop., Vol. 43, No.5/6, September/November 1999, pg. 829 - 846.		<input type="checkbox"/>	<input type="checkbox"/>
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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE O 1 LIST OF MATERIALS CITED BY APPLICANT <small>(use several sheets if necessary)</small>			ATTY. DOCKET NO.: 57941.000025		SERIAL NO.: 10/662,872	
			INVENTOR'S NAME: Vladimir M. STOJANOVIC et al.		EXAMINER: Unassigned	
			FILING DATE: September 16, 2003		GROUP: 2816	
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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
CO	1. 3775688	11-27-1973	Hinoshita et al.			
CO	2. 3992616	11-16-1976	Acker			
CO	3. 4709376	11-24-1987	Kage			
CO	4. 4730343	03-08-1988	Kanemasa et al.			
CO	5. 5268930	12-07-1993	Sendyk et al.			
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CO	12. 5875177	02-23-1999	Uriu et al.			
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CO	18. 20030016091	01-23-2003	Casper			
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CO	20. 20030070126	04-10-2003	Werner et al.			
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CO	25. 20030159094	08-21-2003	Smith et al.			
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CO	35.	03/025599 A1		Werner et al.	03-27-2003	WO																																																																															
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*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
CO		US- 5,191,462	03-02-1993	Gitlin et al.	
CO		US- 4,615,038	09-30-1986	Lim, deceased et al.	
		US-			

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			YES	NO
CO		Aaron MARTIN et al., "8Gb/s Differential Simultaneous Bidirectional Link with 4mV 9ps Waveform Capture Diagnostic Capability," ISSCC 2003/Session 4/Clock Recovery and Backplane Transceivers/Paper 4.5, 2003 IEEE International Solid-State Circuits Conference	<input type="checkbox"/>	<input type="checkbox"/>
CO		Sanjay KASTURIA et al., "Techniques for High-Speed Implementation of Nonlinear Cancellation," IEEE Journal on Selected Areas In Communications, vol. 9, no. 5, June 1991, pp. 711-717	<input type="checkbox"/>	<input type="checkbox"/>
CO		Jack H. WINTERS et al., "Adaptive Nonlinear Cancellation for High-Speed Fiber-Optic Systems," Journal of Lightwave Technology, vol. 10, no. 7, July 1992 IEEE, pp. 971-977.	<input type="checkbox"/>	<input type="checkbox"/>
CO		Keshab K. PARHI, "Pipelining in Algorithms with Quantizer Loops," IEEE Transactions on Circuits and Systems, vol. 38, no. 7, July 1991, pp. 745-754	<input type="checkbox"/>	<input type="checkbox"/>
CO		Keshab K. PARHI, "High-Speed Architectures for Algorithms with Quantizer Loops," IEEE, 1990, pp. 2357-2360	<input type="checkbox"/>	<input type="checkbox"/>
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